

FIG. 1

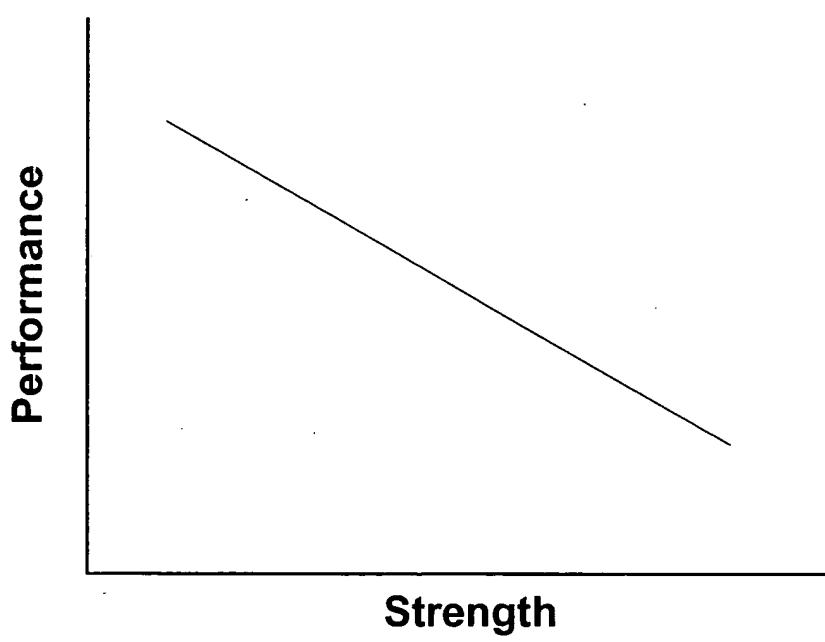


FIG. 2

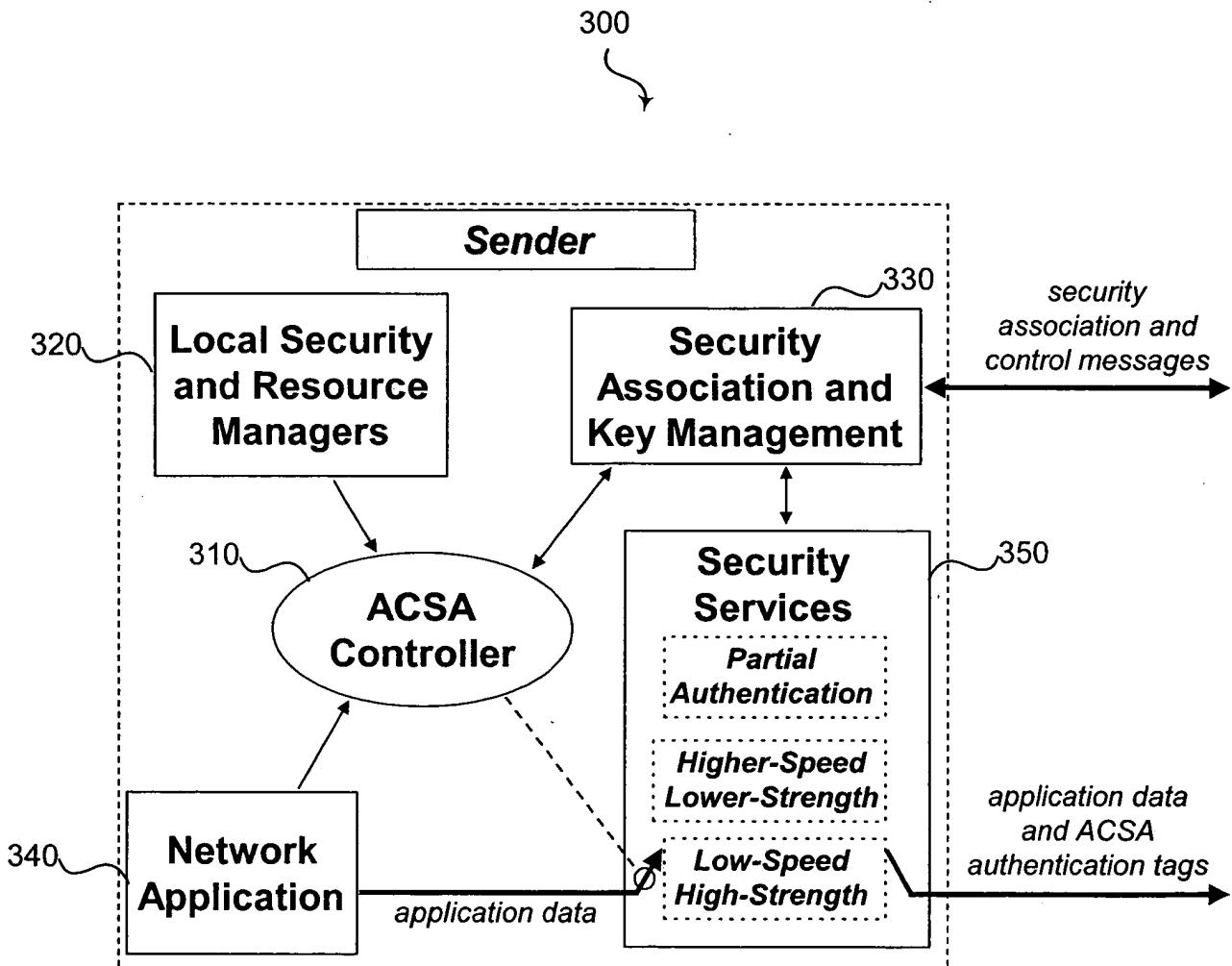


FIG. 3

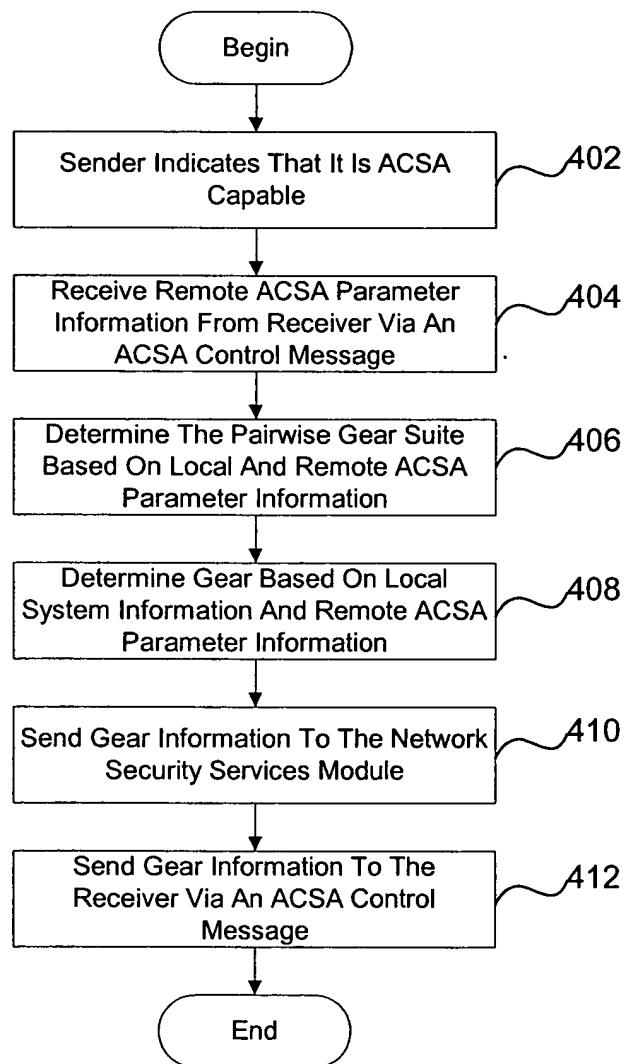


FIG. 4

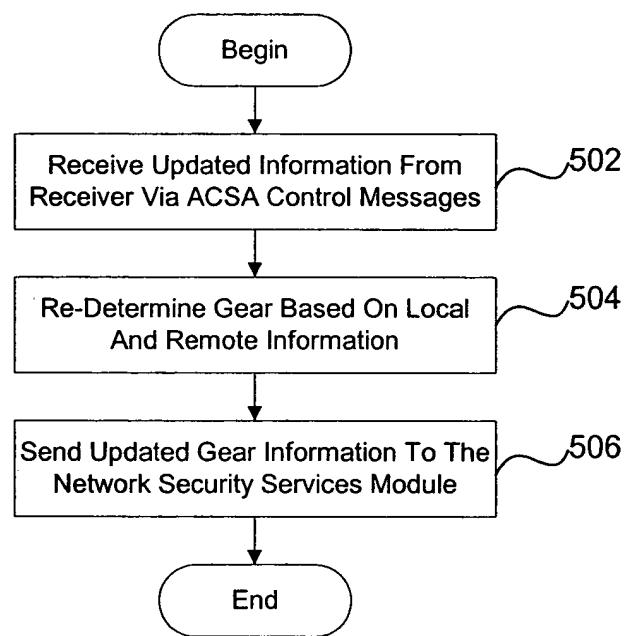


FIG. 5

		<i>Receiver Processor Utilization</i>		
		Too Heavily Loaded	Near Desired CPU Load	Lightly Loaded
<i>Sender Processor Utilization</i>	Too Heavily Loaded	switch to less computationally intensive gear	switch to less computationally intensive gear	switch to less computationally intensive gear
	Near Desired CPU Load	switch to less computationally intensive gear	maintain current gear	maintain current gear
	Lightly Loaded	switch to less computationally intensive gear	maintain current gear	switch to more secure (more computationally intensive) gear

FIG. 6

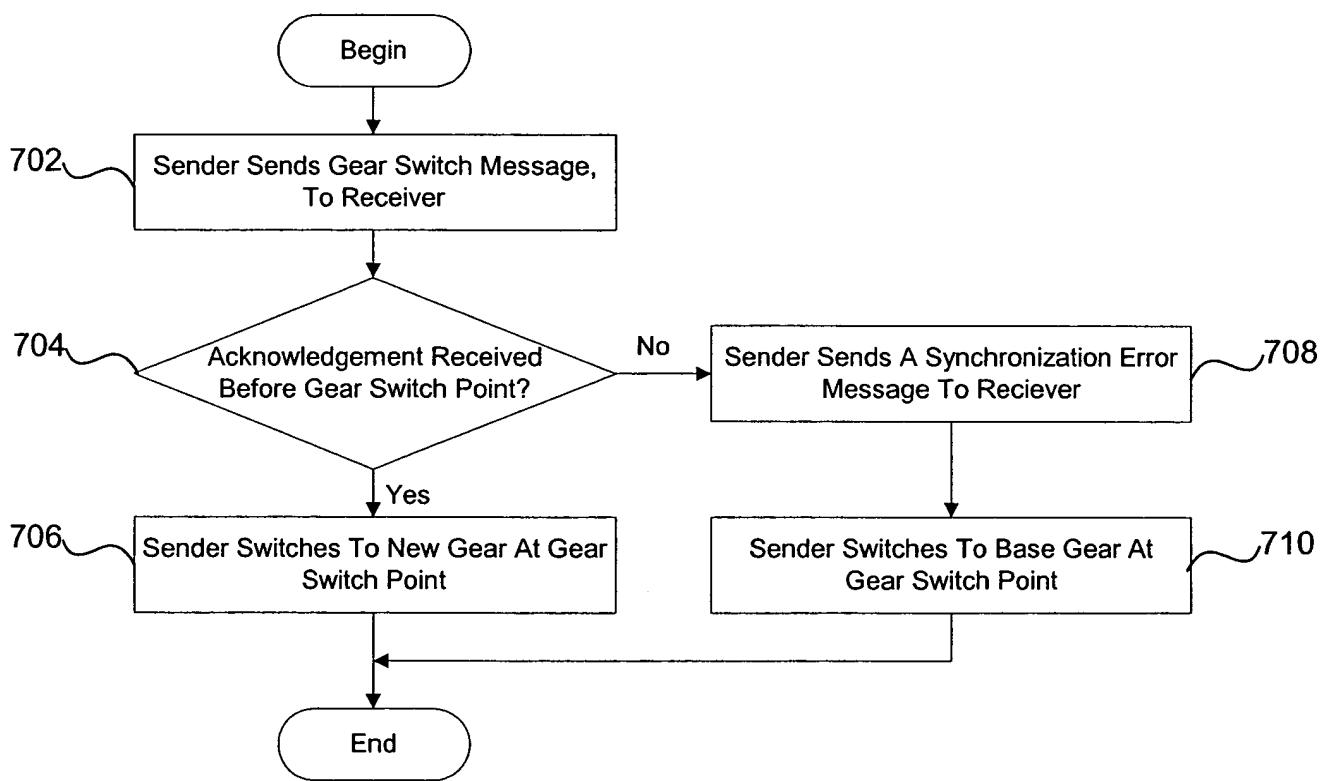


FIG. 7A

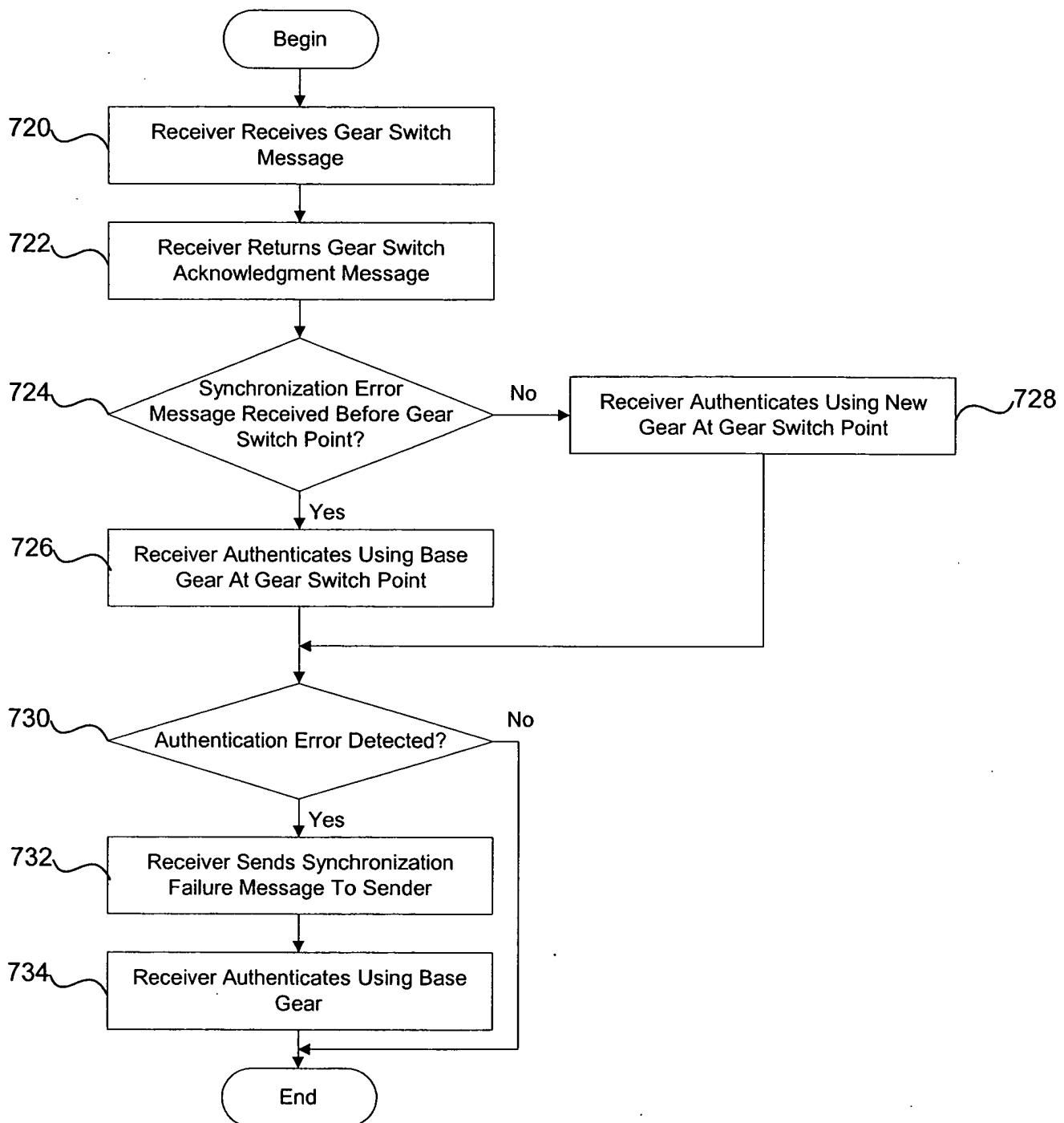


FIG. 7B

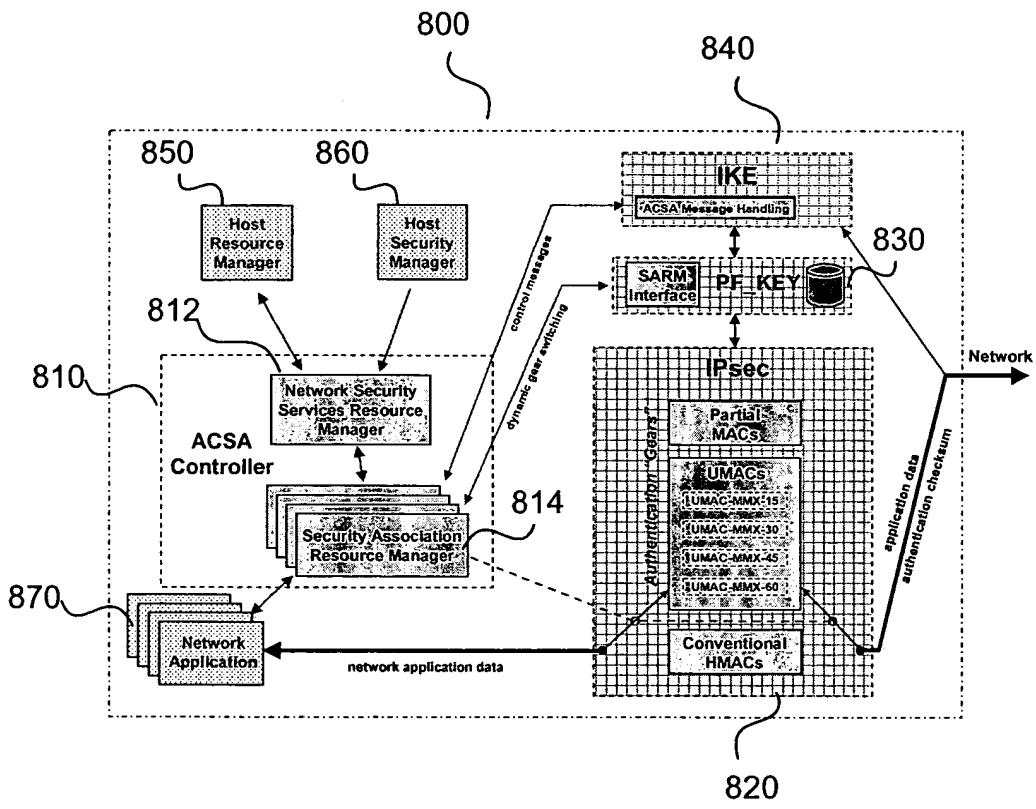


FIG. 8

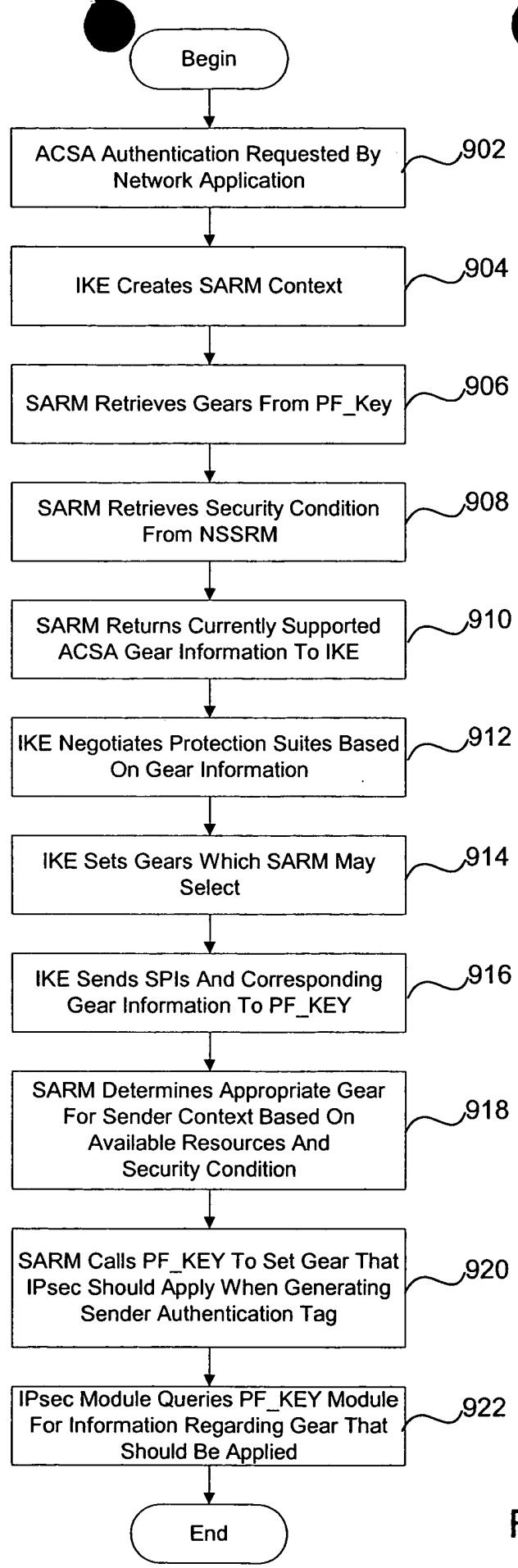


FIG. 9

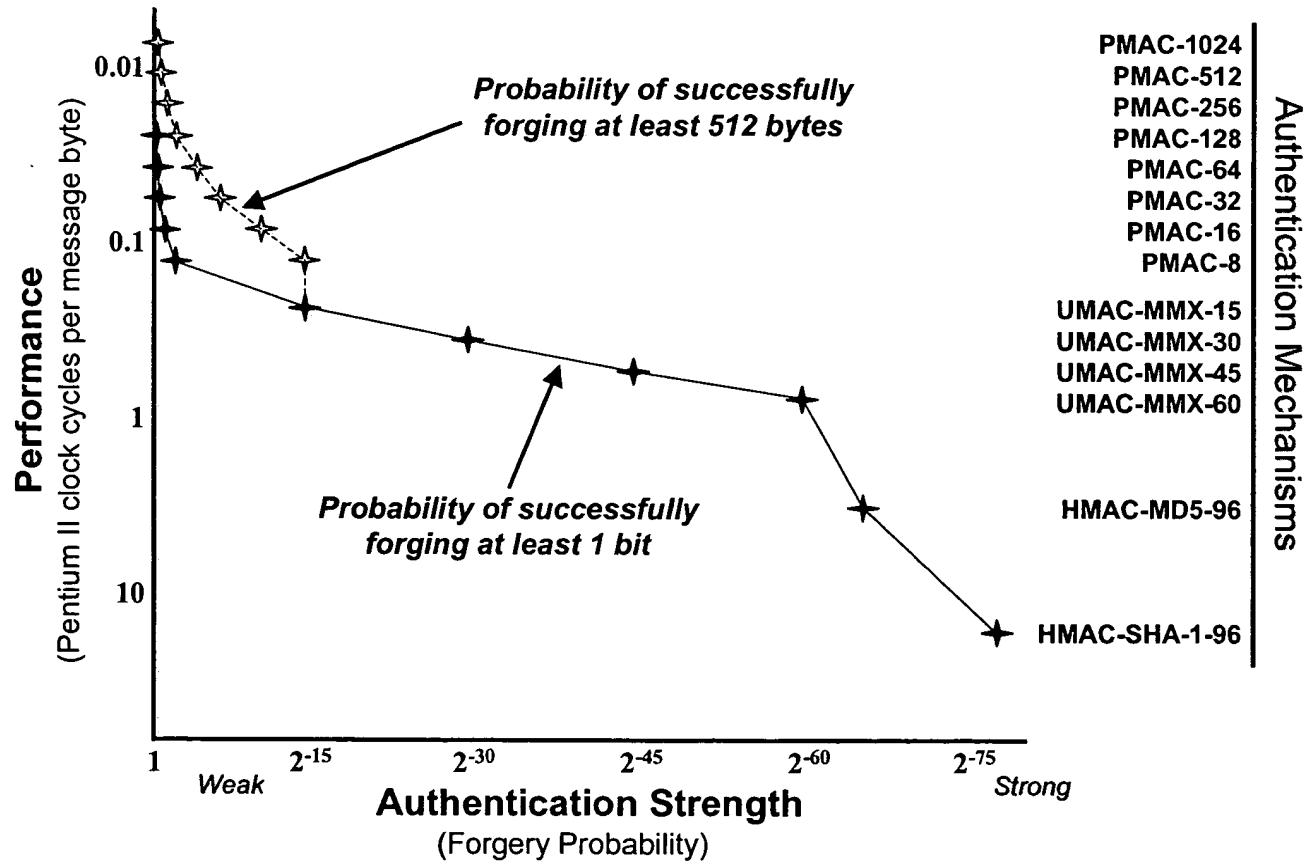


FIG. 10

09621060 - 072100

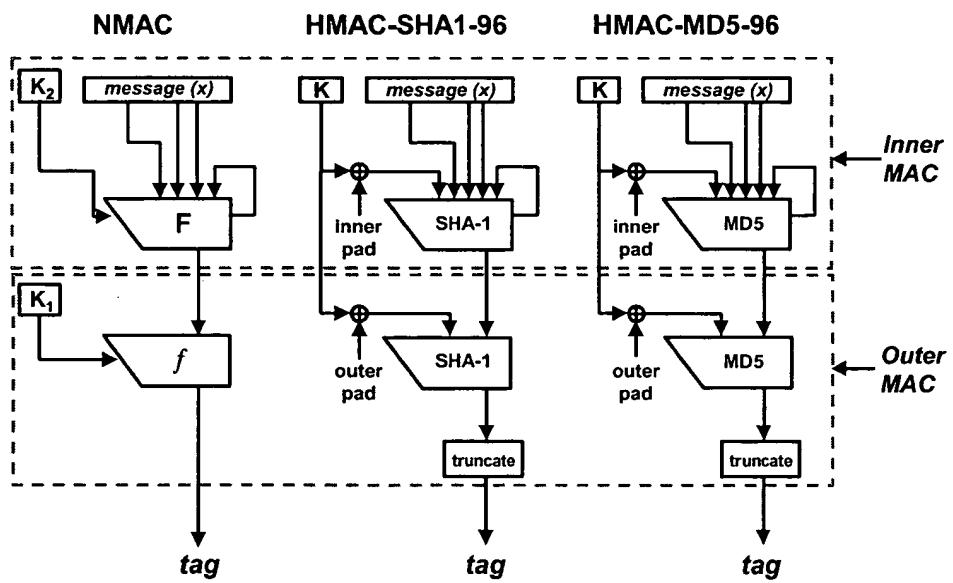


FIG. 11

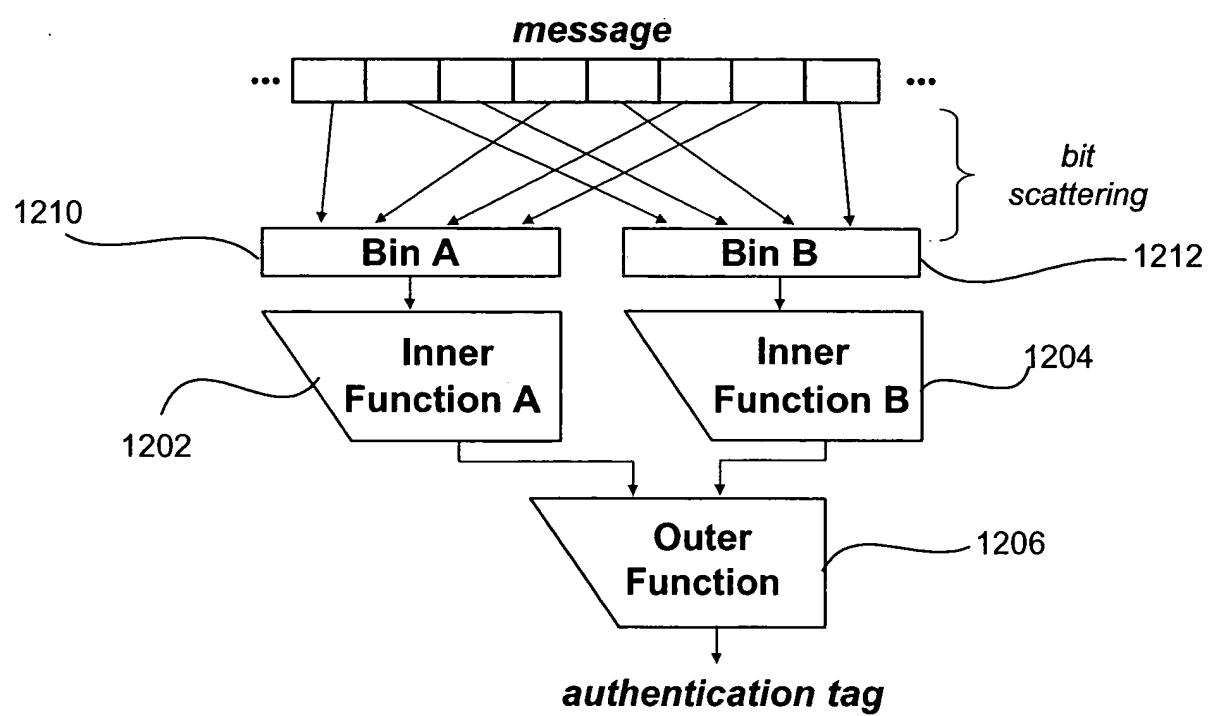


FIG. 12

03623.060 "0321.00

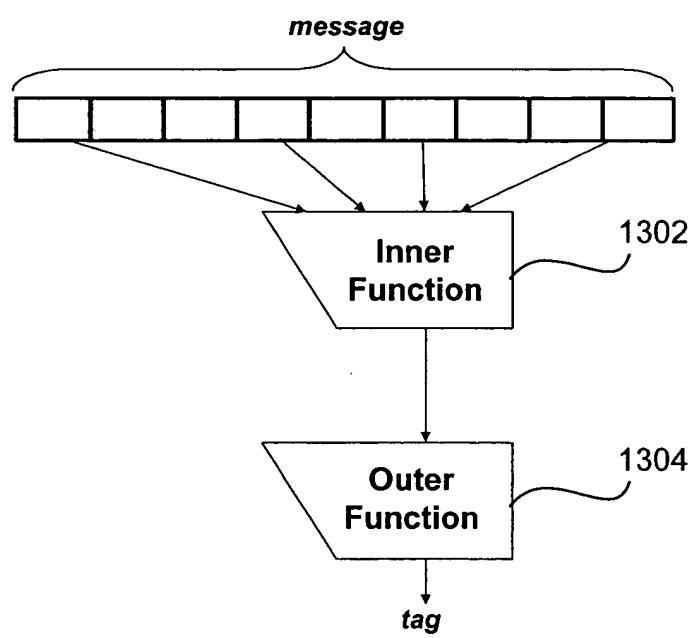


FIG. 13

00621060-0320100

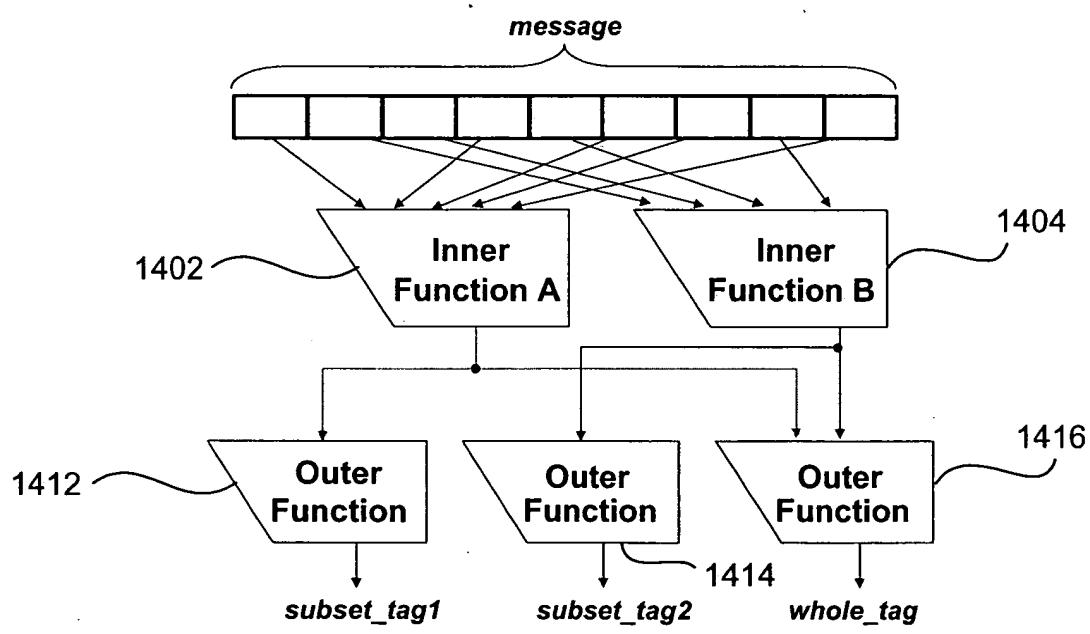


FIG. 14

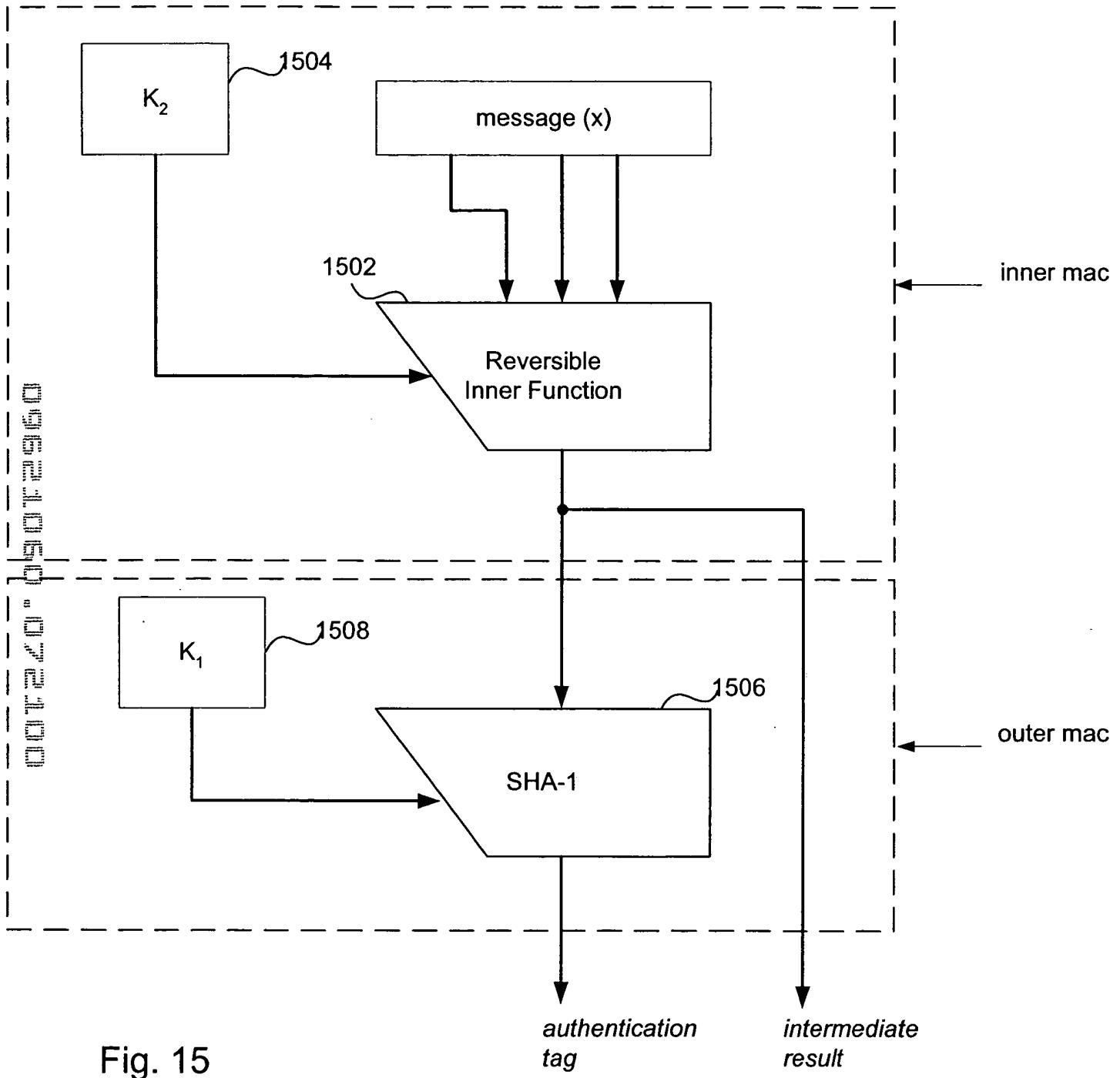
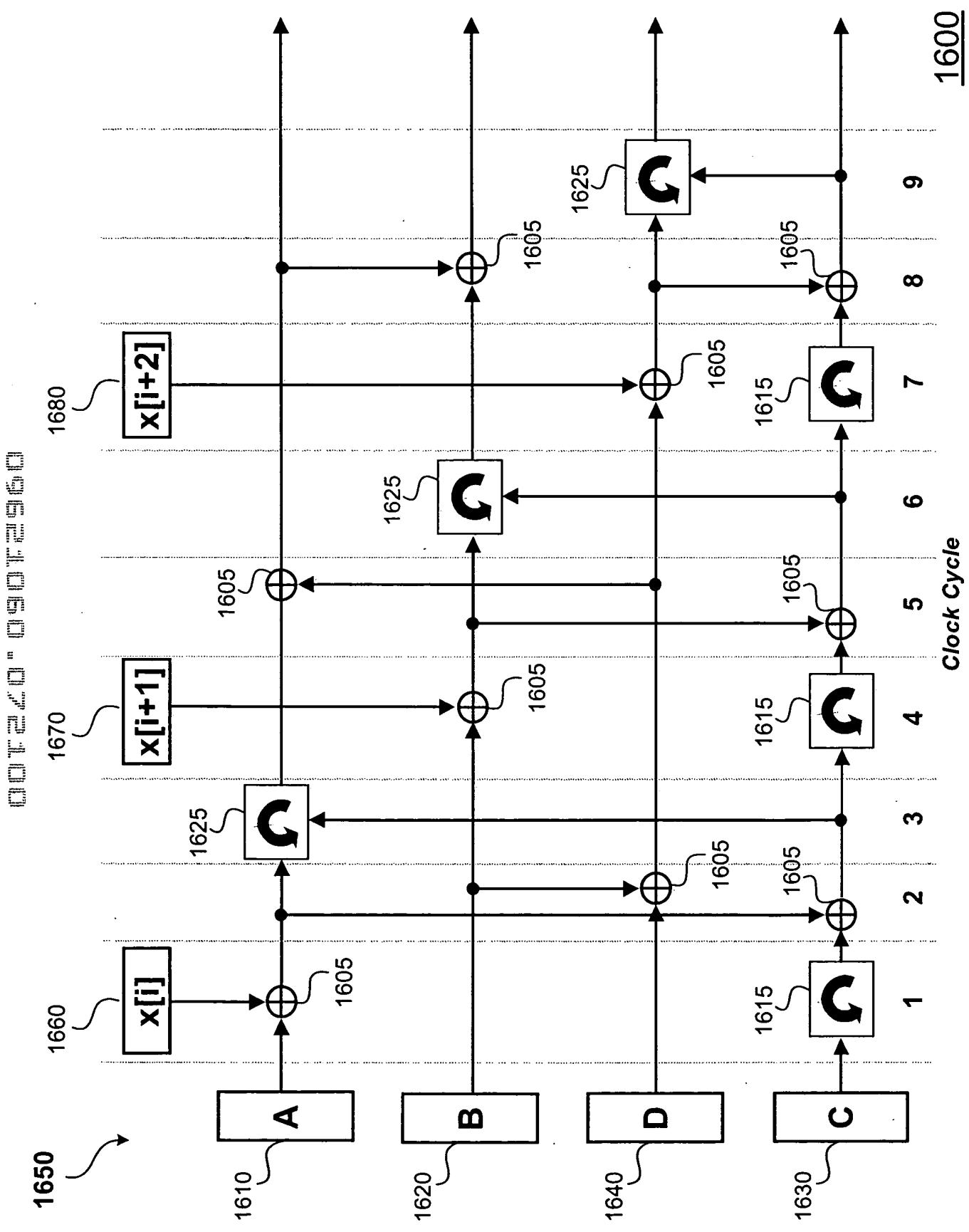


Fig. 15

FIG. 16



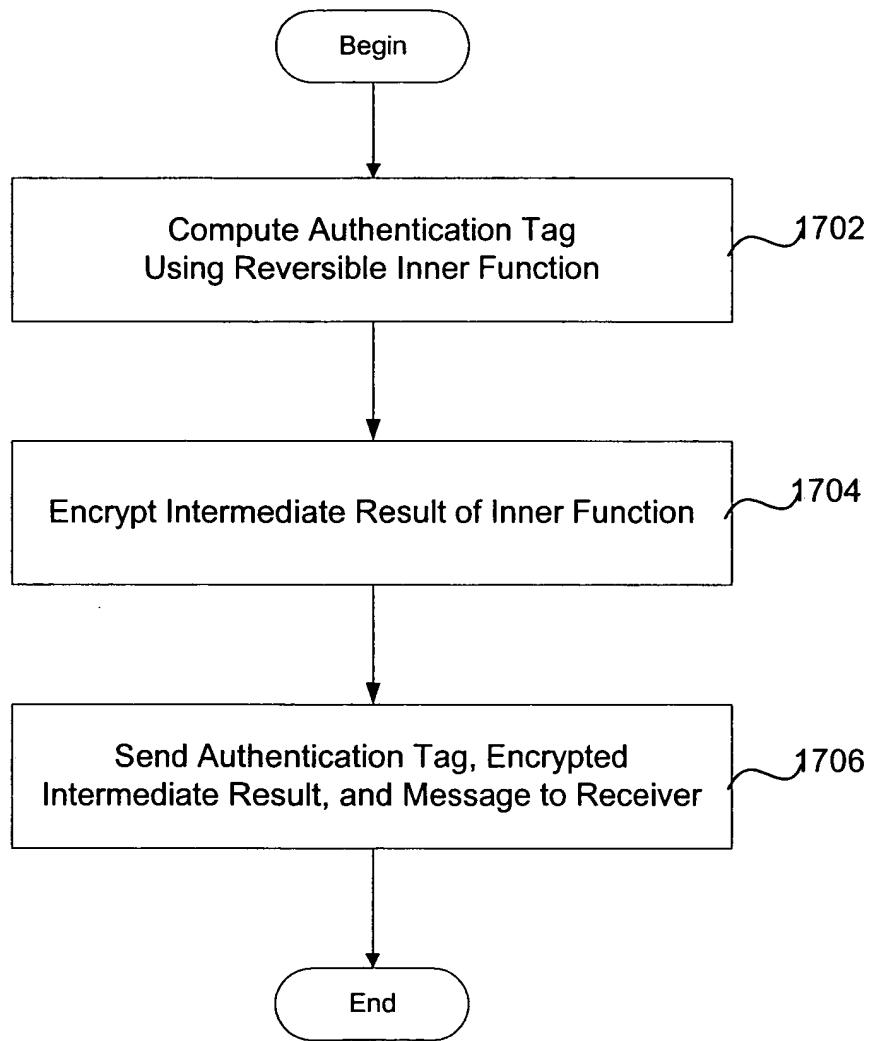


FIG. 17A

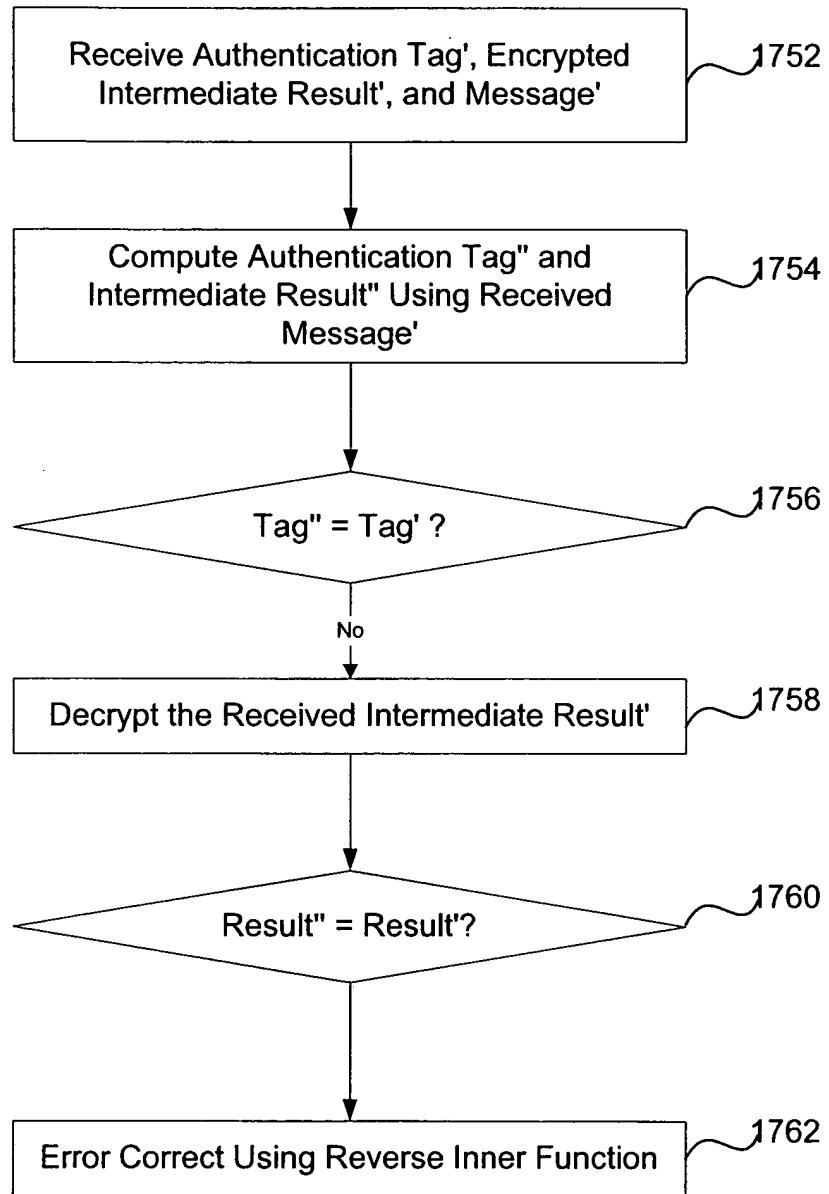


FIG. 17B